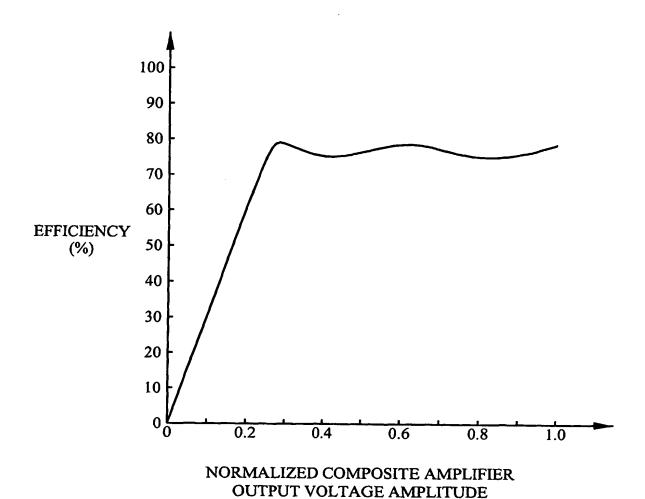


**OUTPUT VOLTAGE AMPLITUDE** 



Fig, 10



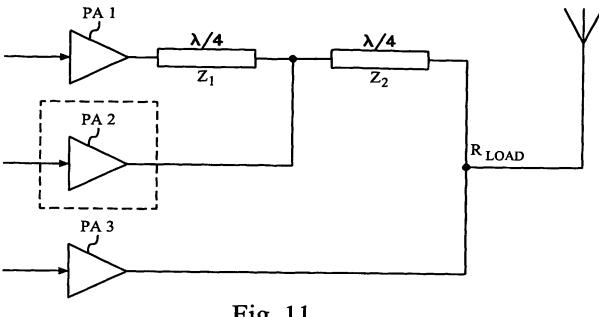
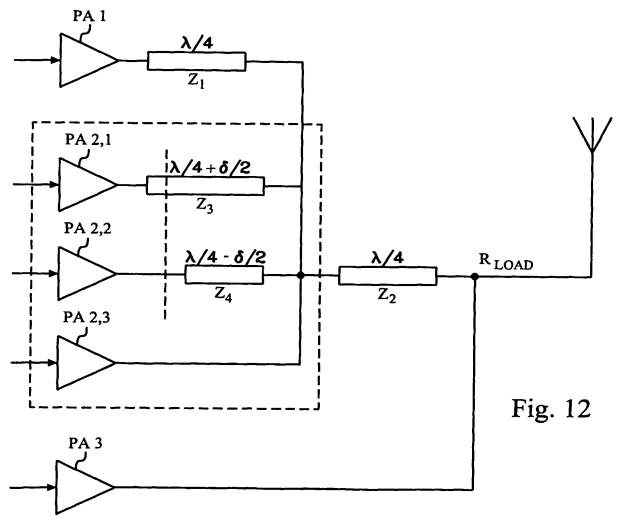
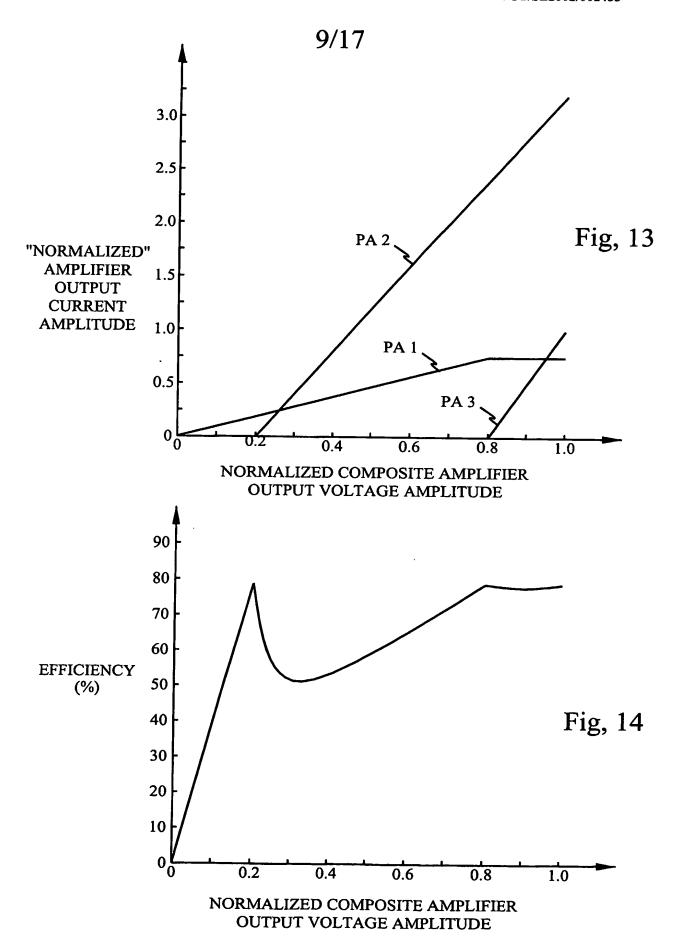
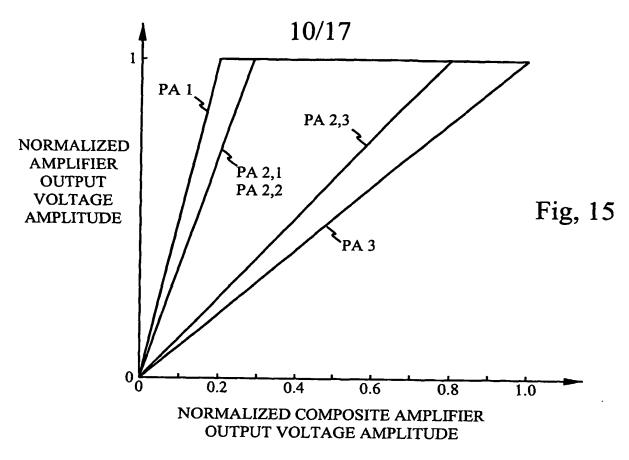
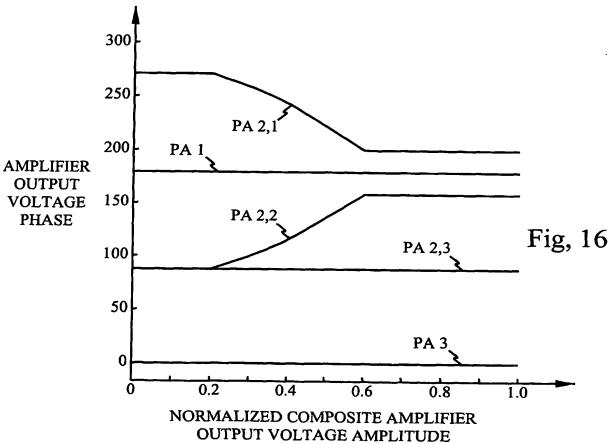


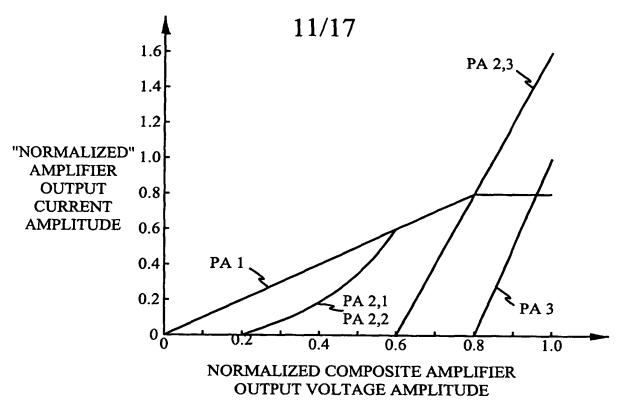
Fig. 11 (PRIOR ART)



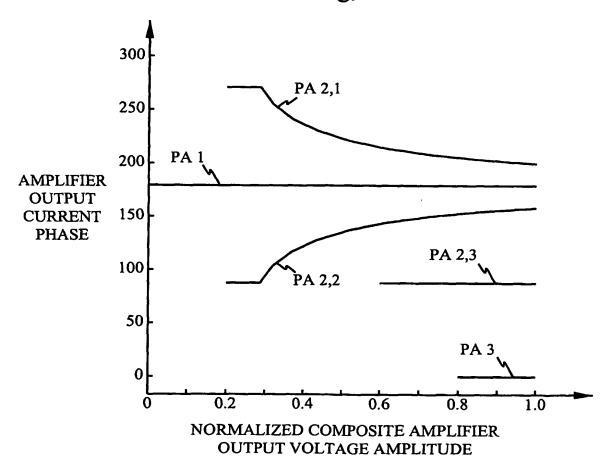




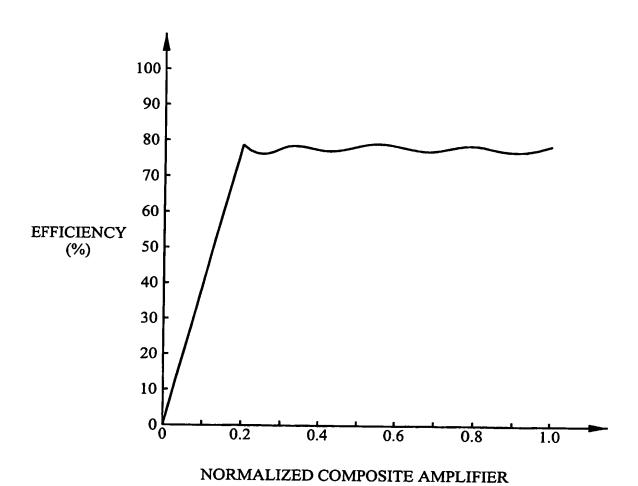




Fig, 17



Fig, 18



Fig, 19

**OUTPUT VOLTAGE AMPLITUDE** 

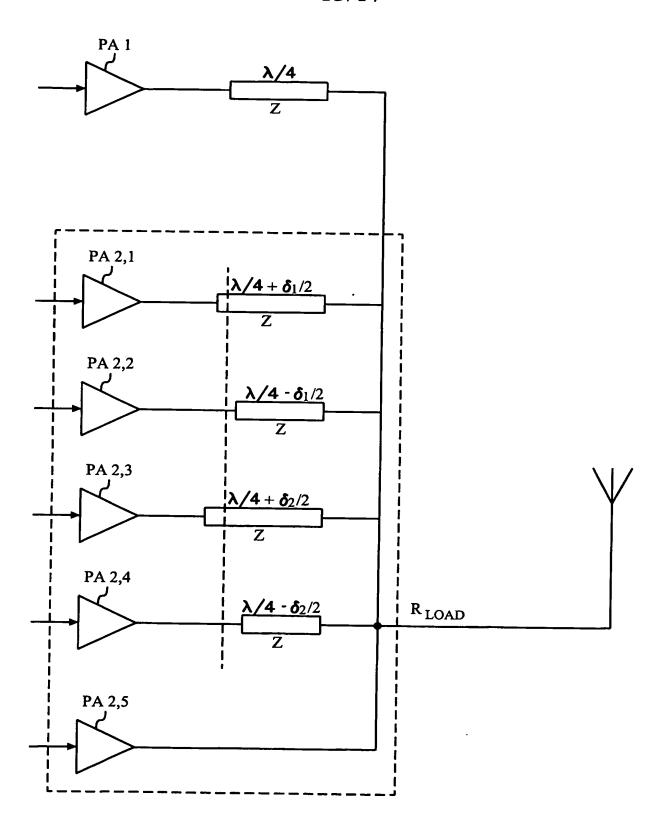
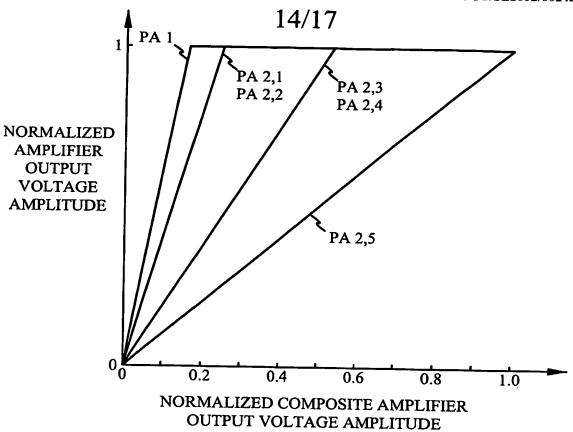
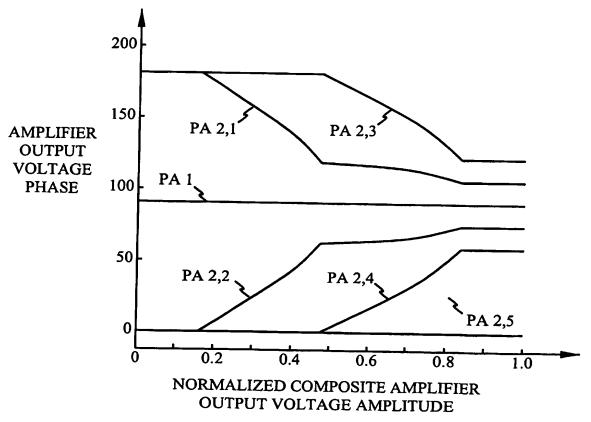


Fig. 20

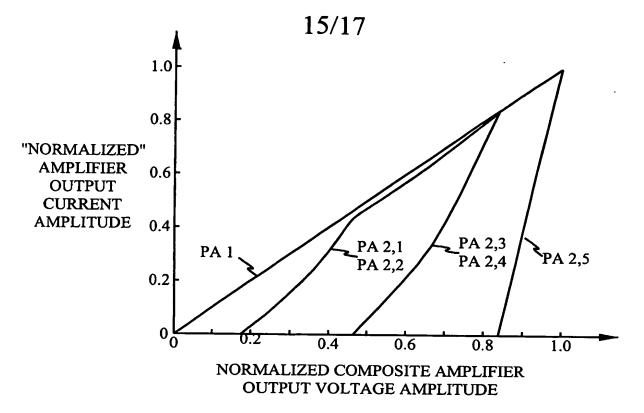
1



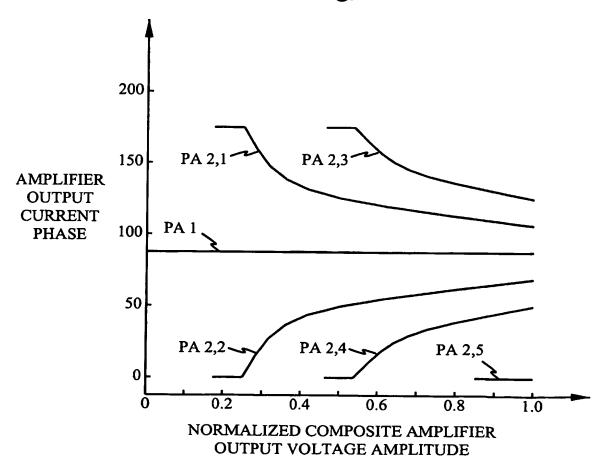
Fig, 21



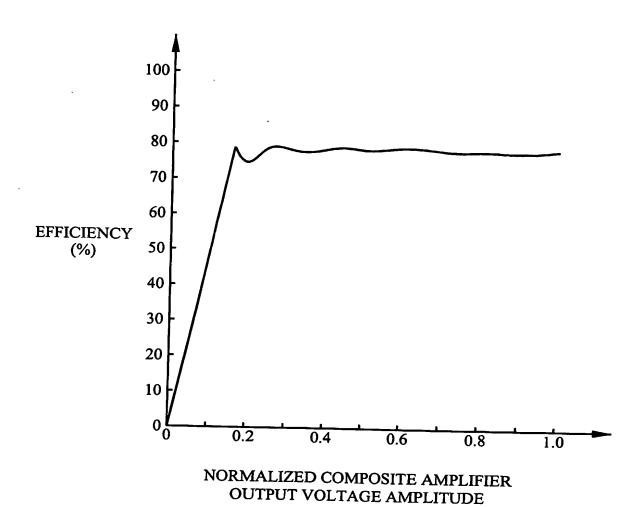
Fig, 22



Fig, 23



Fig, 24



Fig, 25

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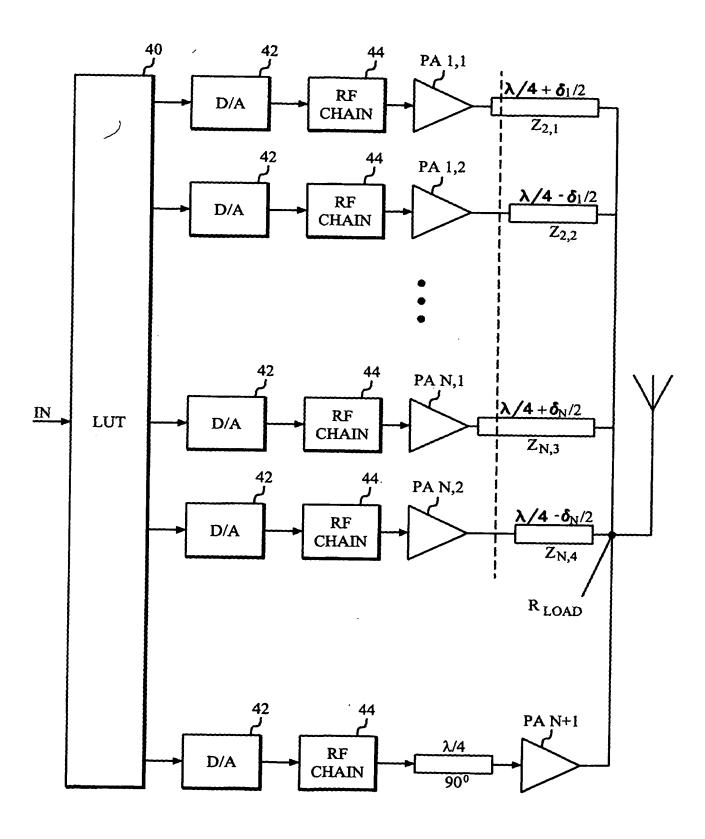


Fig. 26